## **PK70 EX**

## Embedded Control Device

100 Version



# DATASHEET

• The power of the NetBurner's development

**suite:** Customize with a development kit and begin writing application code immediately

## **Key Points**

- The design of a finished product: Metal enclosure, customizable logo, built-in power supply
- The flexibility of a module: design your own internal board or use a NetBurner Personality Blade

### **Device Connectivity**

- 10/100Mbps Ethernet
- UART, I<sup>2</sup>C, and SPI
- SD/MMC flash card support

### Performance and memory

• 32-bit 147.5 MHz Processor

### **Companion development kit**

The following is available with the development kit:

- Customize any aspect of operation including web pages, data filtering, or custom network applications
- Development software: NB Eclipse IDE, Graphical debugger, deployment tools, and examples
- Communication software: TCP/IP stack, SSL/TLS 1.3, HTTP web server, FTP, E-mail, and flash file system

14 digital I/Os

• 5-bit address bus and 8-bit data

bus with 3-chip selects

8MB SDRAM and 4MB Flash

• System software: NBRTOS, ANSI C/C++ compiler and linker







## **Specifications**

#### Processor

32-bit Freescale ColdFire 5270 CPU running at 147.5 MHz

#### Storage

SD/MMC Flash Card Interface with SDHC support (requires exclusive use of SPI signals)

#### Network Interface 10/100 BaseT with RJ-45 connector

#### Data I/O Interface (P1)

- UART
- I<sup>2</sup>C
- SPI
- 14 digital I/O

- 2 external timer in or 3 external timer outputs
- SD/MMC flash card ready
- 5-bit address bus and 8-bit data bus with 3 chip selects
- 3 external IRQs

#### **Serial Configurations**

The UART can be configured in the following way:

- 1 TTL port
- Add external level shifter for RS-232

Note: Additional baud rates and higher serial speeds possible with blade boards

#### **DEBUG Serial Port**

RS-232 with up to 115,200 baud

LEDs Link, Speed/Data, Power

**Physical Characteristics** Dimensions (inches): 4.4" x 3.9" x 1.2"

#### Power

DC Input Voltage: 12V@150mA, 7-24V +5V, +3.3V and raw input voltage are available for use by blade boards **Note:** The PK70 current is exclusive of the Personality Blade Board.

## Environmental Operating Temperature -40° to 85° C

#### **RoHS Compliance**

The Restriction of Hazardous Substances guidelines ensure that electronics are manufactured with fewer environment harming materials.

Agency Approvals UL, C/UL, CE, FCC







## **Part Numbers**

PK70 EX Embedded Control Device Part Number: NBPK70EX-100IR

**DIN Rail Mounting Kit (100 Version, double sided)** Part Number: DIN-100

DIN Rail Mounting Kit (200 Version, single sided) Part Number: DIN-200

**PK70 Development Kit** Part Number: NNDK-NBPK70EX-KIT Kit includes all the hardware and software you need to customize the included platform hardware. See NetBurner Store product page for package contents.

#### Note:

The kit does NOT include a NetBurner Personality Blade Board.





## **Personality Blades**

The PK70 EX can be customized to add additional functionality with one of the standard NetBurner Personality Blades, or you can create your own. The Personality Blades are installed inside the PK70 EX enclosure.

#### **FPGA Blade Board**

Part Number: NBPK**X500**-100CR Personality Blade board with a a Xilinx Spartan 3E FPGA.

#### Note:

- 1. FPGA part type: 3CS500EPQF208
- 2. FPGA Digikey/Xilinx Part Number: 122-1520-ND/XC3S500E-4PQG208C

#### Features:

- Hardware layout featuring access to the Xilinx Spartan 3E FPGA 500K 208-PQFP
- Parallel interface between the Spartan 3E and a PK70 EX device
- High Density 62-pin connector

- Program the FPGA anytime with the JTAG connector or at runtime from a NetBurner application
- Code examples demonstrating how to load an FPGA binary file at runtime

For additional details, please see the NetBurner FPGA Blade Board Manual.

#### Multi-I/O Blade Board

Part Number: NBPKBM-100CR

Personality Blade board with 8 analog to digital converters (ADC), 2 digital to analog converters (DAC), and 16 digital I/O.

Features:

- Eight 12-bit ADC have programmable voltage range of +/-10V, 0 to 10V, +/-5V and 0 to 5V
- 16 digital I/O lines are jumper selectable to 3.3 or 5V
- Two 16-bit DAC has 0 to 4.096V outputs

For additional details, please see the NetBurner Multi-I/O Blade Board Reference Guide.

#### Programmable Xilinx Digital I/O Blade Board

Part Number: NBPKBD-100CR

Personality Blade board with 32 channels of general purpose I/O and a Xilinx CPLD.

#### Features:

- 32 channel digital I/O board
- Each channel is individually programmable to be Hi, Low, Hiz, or input
- Each channel has its own 74HCT125

driver for 20Ma of drive

- Jumper selectable to be 3.3 or 5V out, and 5V tolerant input
- Includes programmable Xilinx CPLD

Part Number: NBPKBU-232CR

Features:

- RS-232 serial device support
- Source code for the factory application is included with any PK70 EX development kit. It includes

For additional details, please see the PK70EX232 Users Manual and PK70ex232 Datasheet.

## Quad UART Blade Board (485 Version)

Part Number: NBPKBU-485CR Personality Blade board with 4 RS-485 UARTs.

Features:

- RS-485 serial device support
- Source code for the factory application is included with any PK70 EX development kit. It includes

For additional details, please see the PK70EX485 Users Manual and PK70ex485 Datasheet.

## Quad UART Blade Board (232/422/485 Multi Mode Serial Version)

Part Number: NBPKBU-MMSCR

Personality Blade board with 4 RS-232 or RS-485 UARTs.

Features:

- RS-232/422/485 serial device support
- Source code for the factory application is included with any PK70 EX development kit. It includes

support for TCP/UDP/Telnet modes, DHCP/Static IP modes, and custom serial packetization options.

· Four serial ports

For additional details, please see the PK70EXMMS Users Manual and PK70exMMS Datasheet.



support for TCP/UDP/Telnet modes, DHCP/Static

IP modes, and custom serial packetization options.

support for TCP/UDP/Telnet modes, DHCP/Static IP modes, and custom serial packetization options.

Four serial ports

Quad UART Blade Board (232 Version)

Personality Blade board with 4 RS-232 serial ports.

**PK70 EX** 

Four serial ports





#### NTP Time Server with GPS Blade Board Part Number: NBPKBG-100CR Personality Blade board with NTP Network Time Server

Features:

- Network Time Protocol (NTP) time server
- Precision GPS time reference keeps the system clock accurate

- Web page configuration
- High performance GPS receiver and antennae connector

For additional details, please see the GPS Blade Board Users Manual and NTP Time Server Users Manual.

#### **Prototype Blade Board**

Part Number: NBPKB**P**-100CR Personality Blade board with a prototype area, 40-pin dual row right angle header, and DB-37 female connector.

Features:

Prototype area

- DB-37 Female Connector
- 40-pin dual row right angle header

For additional details, please see the Bare Personality Blade Board Mechanical Drawing.

#### **NetBurner Personality Blade Development Program**

The NetBurner PK70 product can be easily customized to suit your application requirements by creating a custom "Personality Blade". NetBurner has created a prototype and production design service to design and build Personality Blades at a very low cost, with free Non-Recurring Engineering (NRE) hardware design costs for production orders.

Features:

- NetBurner Engineers work with you on the design concept
- Prototypes and low level software

drivers will be delivered to you

• We can then build production units, or you can build your custom Personality Blade at your own facility

For additional details, please see the PK70 Personality Blade Development Program Datasheet.

## **Ordering Information**

E-mail: sales@netburner.com Online Store: www.Netburner.com Telephone: 1-800-695-6828



## **The Internal Personality Blade Connector**

An internal connector enables you to quickly and easily add additional functionality with one of our standard NetBurner Personality Blades, or a blade board you create on your own. Table 1 provides descriptions of pin function of internal NetBurner Personality Blade interface connector. Refer to Figure 1-2 for the appropriate connector pin assignments.

#### Table 1: Internal NetBurner Personality Blade Board Interface Connector Signal Descriptions (1)

Pin	CPU			J1 Connector								
	Pin	Function 1	Function 2	Function 3	General Purpose I/O	Description	Max Voltage					
1		VCC3V				Available power 3.3V@750mA	3.3V					
2		GND				Ground	-					
3	J13	R/W				Read / NOT Write	3.3V					
4	N6	ŌĒ				Output Enable	3.3V					
5	N13	RESET				Processor Reset Input	3.3V					
6	H11	TA			PBUSCTL6	Transfer Acknowledge	3.3V					
7		BUFCLK				Buffer Clock Out (CLKOUT-73.728 Mhz) <sup>2</sup>	3.3V					
8	P13	RSTOUT				Processor Reset Output	3.3V					
9	J1	DB25				Data Bus - Data 25	3.3V					
10	J2	DB24				Data Bus - Data 24	3.3V					
11	H3	DB27				Data Bus - Data 27	3.3V					
12	H4	DB26				Data Bus - Data 26	3.3V					
13	H1	DB29				Data Bus - Data 29	3.3V					
14	H2	DB28				Data Bus - Data 28	3.3V					
15	G1	DB31				Data Bus - Data 31	3.3V					
16	G2	DB30				Data Bus - Data 30	3.3V					
17	G12	3VA1				Data Bus - Address 1 <sup>3</sup>	3.3V					
18	G13	3VA0				Data Bus - Address 0 <sup>3</sup>	3.3V					
19	F14	3VA3				Data Bus - Address 3 <sup>3</sup>	3.3V					
20	G11	3VA2				Data Bus - Address 2 <sup>3</sup>	3.3V					

#### Note:

2. The CLKOUT signal is 1/2 the system frequency of 147.456 MHz.

3. Address lines (3VAx) are 3.3V only.

<sup>1.</sup> Active low signals, such as  $\overline{\text{RESET}},$  are indicated with an overbar



PinCPU PinFunction 1Function 2Function 3General Purpose I/ODescription21B10CS1PCS1Chip Select 122F133VA4PCS3SD_CS1Data Bus - Address 4223A9CS3SD_CS1Chip Select 324C9CS2SD_CS0Chip Select 225J12J12 C_SDAUART1_RXPFECI2C126J11J2C_SCLUART1_TXPFECI2C027L6T1INT1OUTDREQ128G14TOUT3SPI_CS329L8IRQ3PILQ320N8IRQ3PIRQ321N7IRQ7PIRQ722A6SPI_CS0PQSPI324SPI_CS3POSPI	
22F133VA4Data Bus - Address 4²23A9CS3SD_CS1Chip Select 324C9CS2SD_CS0Chip Select 225J12 D8I2C_SDAUART1_RXPFECI2C1I²C Data Line³ or UART 1 Receive26J11 D9I2C_SCLUART1_TXPFECI2C0I²C Clock Line³ or UART 1 Transmit27L6 C8T1INT1OUTDREQ1PTIMER3Timer Input 1 or Timer Output 1 or DMA Req28G14TOUT3SPI_CS3PTIMER6Timer Out 3 or SPI Chip Select 3⁵29L8IRQ1PIRQ1External Interrupt 1⁴30N8IRQ3PIRQ3External Interrupt 3⁴31N7IRQ7PIRQ7External Interrupt 7⁴	Max Voltage
23A9 $\overline{CS3}$ $SD_CS1$ Chip Select 324C9 $\overline{CS2}$ $SD_CS0$ Chip Select 225 $J_{12}^{12}$ $I2C_SDA$ UART1_RXPFECI2C1 $I^2C$ Data Line³ or UART 1 Receive26 $J_{11}^{11}$ $I2C_SCL$ UART1_TXPFECI2C0 $I^2C$ Clock Line³ or UART 1 Transmit27 $L_6^{68}$ T1INT1OUT $\overline{DREQ1}$ PTIMER3Timer Input 1 or Timer Output 1 or DMA Req28G14TOUT3SPI_CS3PTIMER6Timer Out 3 or SPI Chip Select 3529L8 $\overline{IRQ1}$ PHRQ3External Interrupt 1430N8 $\overline{IRQ3}$ PHAPIRQ331N7 $\overline{IRQ7}$ PHAPIRQ7	3.3V
24C9CS2SD_CS0Chip Select 225J12 D8I2C_SDAUART1_RXPFECI2C1I2C Data Line3 or UART 1 Receive26J11 D9I2C_SCLUART1_TXPFECI2C0I2C Clock Line3 or UART 1 Transmit27L6 C8T1INT1OUTDREQ1PTIMER3Timer Input 1 or Timer Output 1 or DMA Req28G14TOUT3SPI_CS3PTIMER6Timer Out 3 or SPI Chip Select 3529L8IRQ1PIRQ1External Interrupt 1430N8IRQ3PIRQ3External Interrupt 3431N7IRQ7PIRQ1PIRQ7	3.3V
25J12 D8I2C_SDAUART1_RXPFECI2C1I²C Data Line³ or UART 1 Receive26J11 D9I2C_SCLUART1_TXPFECI2C0I²C Clock Line³ or UART 1 Transmit27L6 C8T1INT1OUTDREQ1PTIMER3Timer Input 1 or Timer Output 1 or DMA Req28G14TOUT3SPI_CS3PTIMER6Timer Out 3 or SPI Chip Select 3529L8IRQ1PIRQ1External Interrupt 1430N8IRQ3PIRQ3External Interrupt 3431N7IRQ7PIRQ4PIRQ7	3.3V
25   D8   I2C_SDA   OART I_RX   PFECI2C1   PC bata Line* or OART I Receive     26   J11 D9   I2C_SCL   UART1_TX   PFECI2C0   I²C Clock Line³ or UART 1 Transmit     27   L6 C8   T1IN   T1OUT   DREQ1   PTIMER3   Timer Input 1 or Timer Output 1 or DMA Req     28   G14   TOUT3   SPI_CS3   PTIMER6   Timer Out 3 or SPI Chip Select 3 <sup>5</sup> 29   L8   IRQ1   PIRQ1   External Interrupt 1 <sup>4</sup> 30   N8   IRQ3   PIRQ3   External Interrupt 3 <sup>4</sup> 31   N7   IRQ7   PIRQ7   External Interrupt 7 <sup>4</sup>	3.3V
20 D9 120_30L 0ART1_TX PPECI200 PC Clock Life® 0F0ART1 Haishift   27 L6 C8 T1IN T1OUT DREQ1 PTIMER3 Timer Input 1 or Timer Output 1 or DMA Req   28 G14 TOUT3 SPI_CS3 PTIMER6 Timer Out 3 or SPI Chip Select 3 <sup>5</sup> 29 L8 IRQ1 PIRQ1 External Interrupt 1 <sup>4</sup> 30 N8 IRQ3 PIRQ3 External Interrupt 3 <sup>4</sup> 31 N7 IRQ7 PIRQ7 External Interrupt 7 <sup>4</sup>	3.3V
27 C8 THN THOUT DREQT PTIMERS Timer input for timer Output for Divia Req   28 G14 TOUT3 SPI_CS3 PTIMER6 Timer Out 3 or SPI Chip Select 3 <sup>5</sup> 29 L8 IRQ1 PIRQ1 External Interrupt 1 <sup>4</sup> 30 N8 IRQ3 PIRQ3 External Interrupt 3 <sup>4</sup> 31 N7 IRQ7 PIRQ7 External Interrupt 7 <sup>4</sup>	3.3V
29 L8 IRQ1 PIRQ1 External Interrupt 1 <sup>4</sup> 30 N8 IRQ3 PIRQ3 External Interrupt 3 <sup>4</sup> 31 N7 IRQ7 PIRQ7 External Interrupt 7 <sup>4</sup>	uest 1 3.3V
30 N8 IRQ3 PIRQ3 External Interrupt 3 <sup>4</sup> 31 N7 IRQ7 PIRQ7 External Interrupt 7 <sup>4</sup>	3.3V
31     N7     IRQ7     PIRQ7     External Interrupt 7 <sup>4</sup>	3.3V
	3.3V
	3.3V
	3.3V
33 B5 SPI_DIN I2C_SDA PQSPI1 SPI Data In <sup>5</sup> or I <sup>2</sup> C Serial Data <sup>3</sup>	3.3V
34 A5 SPI_DOUT PQSPI0 SPI Data Out⁵	3.3V
35 C5 SPI_CLK I2C_SCL PQSPI2 SPI Clock <sup>5</sup> or I <sup>2</sup> C Serial Clock <sup>3</sup>	3.3V
36 M9 T2IN T2OUT DREQ2 PTIMER5 Timer Input or Timer 2 Output 2 or DMA Req	uest 2 3.3V
37 VCC5V Available Power 5V@1A	5V
38 GND Ground	-
39 VCCRAW DC Input Voltage Power <sup>6</sup>	3.3V
40 GND Ground	-

Note:

2. Address lines (3VAx) are 3.3V only.

3. If using I<sup>2</sup>C, pull-up resistors must be added to open drain SDA/SCL signals.

4. IRQ's pulled up to 3.3V with 4.7K resistor.

5. No pull-ups/down on SPI signals.

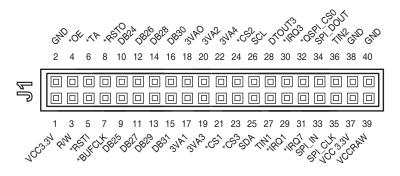
6. Same voltage rail that is used to power the device externally.

<sup>1.</sup> Active low signals, such as RESET, are indicated with an overbar





#### Figure 1: Internal Personality Blade Interface Connector Signal Assignments



#### Figure 2: Personality Blade Board Pin 1\*

#### Note:

Pin 1 is located next to the J1 label and is the pin closest to the PCB edge.

